WEST Search History

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DATE: Tuesday, August 24, 2004

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		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ	
☐	L9	L7 and $1MOS	61
☐	L8	L7 and wiring board	2
☐	L7	L6 and power supply	177
☐	L6	bias voltage and junction temperature	367
☐	L5	L3 and printed board	0
☐	L4	L3 and wiring board	1
☐	L3	L1 and (DUT or device under test)	93
☐	L2	L1 and printed wiring board	29
☐	L1	bias voltage and voltage supply	10634

END OF SEARCH HISTORY

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